

## WEST Search History

DATE: Sunday, October 29, 2006

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=PGPB; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L26	(fail\$ with ("optical rule check\$" or orc))[clm]	0
<input type="checkbox"/>	L25	l21 and L24	0
<input type="checkbox"/>	L24	("optical rule check\$" or orc)[clm]	39
<input type="checkbox"/>	L21	L20 and (layout or pattern)[clm]	26
<input type="checkbox"/>	L20	(simulat\$ with scal\$)[clm]	127
<input type="checkbox"/>	L19	(scal\$ with (next near3 (generation or technology)))[clm]	1
<input type="checkbox"/>	L18	(library with "current technology")[clm]	0
<input type="checkbox"/>	L17	L16 and poor[clm]	0
<input type="checkbox"/>	L16	l6 and manufacturability[clm]	17
<input type="checkbox"/>	L15	L14 and manufacturability[clm]	1
<input type="checkbox"/>	L14	(classif\$ near5 (edge or structure))[clm]	394
<input type="checkbox"/>	L13	(parametrically with varying with (layout or pattern ot wafer))[clm]	0
<input type="checkbox"/>	L12	("design rule" with disallow\$)[clm]	0
<input type="checkbox"/>	L11	l6 and l9	6
<input type="checkbox"/>	L10	"poor manufacturability"[clm]	0
<input type="checkbox"/>	L9	(simulat\$ with (pattern near10 wafer))[clm]	35
<input type="checkbox"/>	L8	l1 and L6	2
<input type="checkbox"/>	L6	"design rules"[clm]	465
<input type="checkbox"/>	L1	"initial layout"[clm]	31

END OF SEARCH HISTORY